

	Type	L #	Hits	Search Text	DBs
22	BRS	L22	1	(first adj delay and second adj delay and processor same (control adj circuit controller) same timing adj circuit).clm.	US- PGPUB
23	BRS	L23	1	first adj delay\$3 same second adj delay\$3 same clock same timing adj signal with processor.clm.	US- PGPUB
24	BRS	L24	1	first adj delay\$3 same second adj delay\$3 same clock same timing adj signal and level adj converter.clm.	US- PGPUB
30	BRS	L30	0	(first adj delay\$3 same second adj delay\$3 same clock and third adj timing adj signal with first adj timing adj signal with clock with period with longer) .clm.	US- PGPUB
31	BRS	L31	0	(first adj delay\$3 same second adj delay\$3 same clock and third adj timing adj signal with first adj timing adj signal with first adj logical adj circuit).clm.	US- PGPUB